

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Number : 10/595,303 Confirmation No.: 7114  
Applicant : Roy KNECHTEL  
Filed : May 19, 2006  
Title : METHOD AND DEVICE FOR SECURE, INSULATED AND ELECTRICALLY CONDUCTIVE ASSEMBLING OF TREATED SEMICONDUCTOR WAFERS  
TC/Art Unit : 2823  
Examiner: : Kevin A. PARENDO  
Docket No. : 60291.000048  
Customer No. : **21967**

**MAIL STOP FEE AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE**

Sir,

The Non-Final Office Action dated June 10, 2011, was received in the above-referenced utility patent application. Applicant respectfully requests that the application be reconsidered in light of the following amendments and remarks.

**Amendments to the claims** begin on page 2 of this Response.

**Remarks/Arguments** begin on page 8 of this Response.